

CLAIMS

1. A method for fabricating a thin-film opto-electronic device on a conductive silicon-containing substrate comprising the steps of growing a sequence of layers including at least the steps of:

10 a) forming a porous silicon layer on said substrate such that said porous silicon layer acts as a light diffuser and as a light reflector;

b) growing a non-porous layer on said porous silicon layer, said non-porous layer comprising at least one first region and at least one second region being formed in said non-porous layer, said first region of a first conductivity type, said second region of
15 a second conductivity type, different from said first conductivity type,

and said sequence of layers being such that optical confinement is realised in said device.

2. A method as recited in claim 1, wherein said first region of a first conductivity type is acting as a light absorber.

3. A method as recited in claim 1, wherein said non-porous layer is a silicon-
20 containing semiconductor layer, preferably a non-porous group IV and/or elemental and/or crystalline layer.

4. A method as recited in claim 3, wherein said non-porous layer is a Si or a SiGe or a Ge layer being grown by means of chemical vapour deposition or physical vapour or molecular beam epitaxy.

25 5. A method as recited in claim 1, wherein said porous silicon layer is formed by exposing the substrate to an electrochemical treatment or a chemical treatment or spark erosion.

6. A method as recited in claim 1, wherein said porous silicon layer has a thickness in the range from 0.1 μm to 1 μm and a porosity in the range from 20% to 70%.

7. A method as recited in claim 1, wherein the porous silicon layer formed
30 comprises porous silicon parts and columnar conductive parts, said columnar conductive parts forming conductive connections between the substrate and the first region of the semiconductor layer; said substrate and said first region having the same conductivity type.

8. A method as recited in claim 7, wherein prior to the formation of the porous silicon layer a patterned mask layer is formed on the substrate to thereby define at least one first
35 area and at least one second area in the substrate, said first area being uncovered, said second

- 5 area being covered with said mask layer; thereafter said porous silicon layer is formed on said first area and said mask layer is removed from said second area; thereafter a semiconductor layer is grown on said second area and on said porous silicon layer thereby creating said columnar conductive parts.
9. A method as recited in claim 1, wherein at least one third region is formed in said first region for contacting said first region, said third region being isolated from said second region and having the same conductivity type as said first region.
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